



SREENIDHI INSTITUTE OF SCIENCE AND TECHNOLOGY
(Autonomous College)
Yamnampet, Ghatkesar, Hyderabad - 501 301
(Affiliated to JNTUH, Hyderabad)

Date: 26-09-2020

BOARD OF STUDIES MEETING

The BOS members of the B.Tech-ECE, SNIST have meet on 26-09-2020 at 2 PM on Google meet.

1. AGENDA

- a. To approve the course structure of B. Tech I-IV Year under A-20 regulation for students admitting in the A.Y 2020-2021.
- b. To approve the detailed syllabus of B. Tech I-II Year under A-20 regulation for students admitting in the A.Y 2020-2021.
- c. To approve the detailed syllabus of service courses being offered by ECE Department under A-20 regulation for students admitting in the A.Y 2020-2021.
- d. To approve the inclusion of Artificial Intelligence and Cyber Security as Mandatory Courses under A-18 regulation.

2. MEMBERS PRESENT

S. No.	Name of the Member
1	Dr. M. Asha Rani, Professor, JNTUH (As she could not attend meeting on 26.09.2020 due to some unavoidable urgent work, we had a meeting with her on 30.09.2020 at her residence)
2	Dr. P. Srihari, Associate Professor, NIT Warangal
3	Dr. P. Chandra Sekhar, Professor, Osmania University
4	Dr. G. Mallikarjun Rao, Scientist F, RCI
5	Mr. M. Pavan Kumar, Project Manager, Infosys
6.	Mr Gaurav Kumar Wankar, Data Scientist, ScriptBees

M. Asha Rani

G. Mallikarjun Rao

S. No.	Name of the Member
1	Dr. SPV. Subba Rao, BOS Chairman

2	Dr. K. Kishan Rao, Professor
3	Dr. Ameet Chavan, Professor
4	Dr. NS. Murti Sarma, Professor
5	Dr. Asha Devi, Professor
6	Prof. K. Venkat Reddy, Professor
7	Dr. S. Krishna, Professor
8	Dr. Chatopadyaya, Professor.
9	Dr. V. Jayaprakasan, Professor
10	Dr. CN. Sujatha, Professor
11	Dr. Vyomal Pandaya, Associate Professor
12	Dr. V. Kumara Swamy, Associate Professor
13	Mr. N. Srinivas Reddy, Associate Professor
14	Mr. T. Venkata Rao, Associate Professor
15	Mrs. V. Sudha Rani, Associate Professor
16	Ms. Shafee Unnisa Syed, Associate Professor
17.	Mr. G. Prasad Acharya, Associate Professor
18.	Mr. K. Jeevan Reddy, Associate Professor
19.	Mr. SN. Chandrashekar, Assistant Professor
20.	Mrs. T. Swapna, Assistant Professor
21.	Mrs. P. Lavanya, Assistant Professor
22.	Mr. L.V.R. Chaitanya Prasad, Assistant Professor
23.	Mr. P. Anvesh, Assistant Professor



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The following resolutions are made by incorporating the suggestions from the BOS MEMBERS:

- The courses Electrical Circuits and Network Analysis ,Electrical Technology shall be combined into one suitable course.
- Merging of Analog communications and Digital Communications (Theory and Lab) into Analog & Digital Communications (theory and lab) has been approved.
- Java Programming shall be offered in III year II semester to accommodate Electromagnetic Waves and Transmission Lines in II Year II Semester, as this subject is an important fundamental course as prerequisite for many other courses as well as for GATE examination.
- Environmental Science and Ecology shall be offered as a mandatory courses and its credits is to be adjusted with other core courses.
- VLSI Design (Theory and Lab) shall be offered in III Year I Semester.
- Artificial Intelligence (Theory and Lab) shall be offered in III Year II Semester.
- Cyber Security (Mandatory Course) shall be offered in III Year II Semester.
- The following courses shall be offered as Professional Electives under VLSI streams:
 - Hardware Scripting languages
 - Analog and Mixed signal IC Design
 - FPGA Architectures and Applications
 - Digital Design Verification with System verilog
 - Low power VLSI Design

- The following courses shall be included in Professional Electives under Signal Processing stream
 - Adaptive signal Processing
 - Bio Medical Signal Processing & Instrumentation
 - Speech Processing



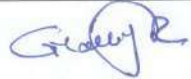


- The following courses shall be offered as open-electives under ECE streams for the students of other branches:
 - Electronics and Instrumentation (OE-I)
 - Fundamentals of Communications (OE-II)
 - Basics of Embedded Systems (OE-III)




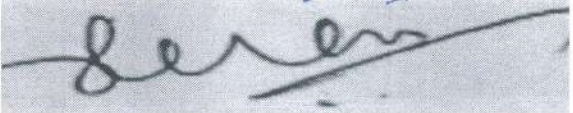
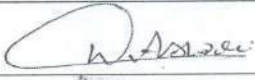

- The following courses shall be offered as open-electives under General streams:
 - 3D Printing (OE-I)
 - Optimization Techniques (OE-II)
 - Robotic Process and Automation (OE-III)










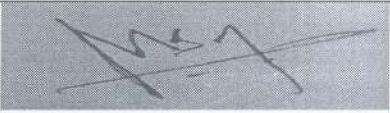

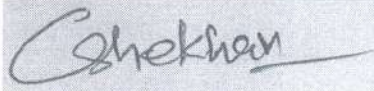
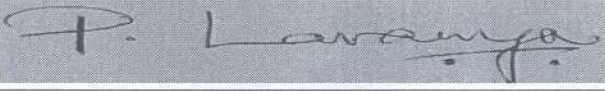

By incorporating the above changes suggested by experts, the course structure of B. Tech I-IV Year under A-20 regulation, the detailed syllabus of B. Tech I-II Year and the service courses being offered by ECE Department under A-20 regulation have been approved


The inclusion of Artificial Intelligence and Cyber Security as Mandatory Courses under A-18 regulation have been approved

Dr. S.P.V Subba Rao
Chairman BOS, SNIST

BOS External Members		
S. No.	Name of the Member	Signature
1	Dr. M. Asha Rani, Professor, JNTUH	
2	Dr. P. Srihari, Associate Professor, NIT Warangal	
3	Dr. P. Chandra Sekhar, Professor, Osmania University	
4	Dr. G. Mallikarjun Rao, Scientist F, RCI	
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2	Dr. K. Kishan Rao, Professor	 Dr K Kishan Rao
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4	Dr. NS. Murti Sarma, Professor	
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(An Autonomous & NAAC Accredited Institution)
YAMNAMPET, GHATKESAR 501 301, RANGA REDDY DIST.
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Date: 07-01-2022

The BOS members of the B.Tech-ECE, SNIST have meet on 07-01-2022 at 2 PM on Google meet.

1. AGENDA

- To approve the course structure of B. Tech III & IV Year under A-20 regulation for students admitting in the A.Y 2020-2021.
- To approve the detailed syllabus of B. Tech III & IV Year under A-20 regulation for students admitting in the A.Y 2020-2021.
- To approve the detailed syllabus of service courses being offered by ECE Department under A-20 regulation for students admitting in the A.Y 2020-2021.
- To approve the inclusion of Minor Course in IOT to be offered by ECE Department as per the guidelines from JNTUH

2. MEMBERS PRESENT

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BOARD OF STUDIES MEETING

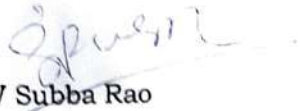
The BOS members of the B. Tech-ECE, SNIST have meet on 07-01-2022 at 2 PM on google meet.

The following modifications are made by incorporating the suggestions from the BOS MEMBERS:


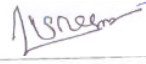



- Python Programming Lab is introduced in III year I semester
- The course Data base management System is shifted to Open Elective-II (CSE stream) and the same is replaced with program core subject entitled Cellular and Mobile Communications.
- Java Programming is shifted into Open Elective-I under CSE stream.
- Two mandatory courses Cyber Security and Artificial Intelligence are included in III Year I Semester and III Year II Semester respectively.
- The credits of Group Project - I (III Year I Semester), Group Project - II (III Year II Semester), Comprehensive Viva Voce (III Year I Semester), Comprehensive Viva Voce (IV Year I Semester) and IOMP are adjusted with other core courses and labs.
- Antennas and Wave Propagations course is shifted to III Year I Semester
- Two new streams have been introduced under professional electives
 - Advanced Computing
 - Microwave and Radar
- The following courses are included as Professional Electives under Advanced Computing stream:
 - Digital Image Processing




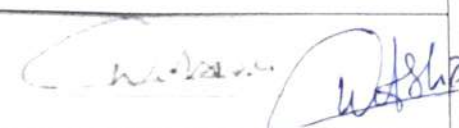
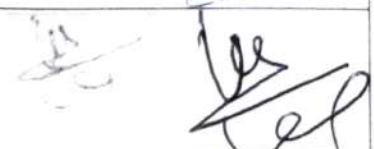

- Artificial Neural Networks
- Computer Vision
- Machine Learning
- Deep Learning
- The following courses are included as Professional Electives under Microwave and Radar stream:
 - Phased Array Antennas
 - Satellite Communications
 - Radar Systems
 - Microwave Integrated Circuits
 - EMI/EMC
- The following courses are included as Professional Electives under VLSI stream:
 - Digital Design Through Verilog in place of Hardware Scripting Language
 - VLSI Physical Design in place of FPGA Architecture and Applications
- The following courses are introduced under Professional Electives Embedded System stream
 - Advanced Computer Architecture
 - Embedded C Programming is shifted to PE-II
- The following courses are introduced under Professional Electives Signal Processing stream
 - Transform Techniques in place of Adaptive Signal Processing
 - Bio-Medical Signal Processing in place of Bio Medical Signal Processing & Instrumentation
 - Radar Signal Processing in place of Speech Processing
- The following courses are included as open-electives under ECE stream for the students of other branches:
 - Electronics and Instrumentation (OE-I)
 - Fundamentals of Communications (OE-II)
 - Electronic Circuit Design and Analysis (OE-III)
- BOS has suggested to offer Intellectual Property Rights as bridge course.
- Introduction of Minor Courses in ECE Department will be discussed in the next BOS meeting








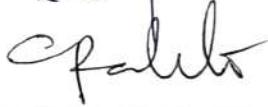


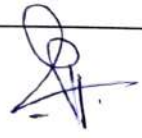
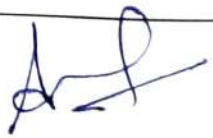
By incorporating the above changes suggested by experts, the course structure of B. Tech III & IV Year under A-20 regulation, the detailed syllabus of B. Tech III & IV Year and the service courses being offered by ECE Department under A-20 regulation have been approved



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B.O.S.