

Curriculum mapping of SNIST (A18 regulation) with VEDA IIT - Industry-Ready Professional Training in VLSI Design/Embedded System Design as approved by JNTUH.

SNIST			VEDA-IIT		Remarks
S.No.	SUB	Credits	SUB	Credits	
IV/IV-B.Tech. ECE-Semester-I					
1	Computer Networks	3	Advanced Logic Design with functional blocks and state machine	4	-
2	Microwave and Optical Communications	2	-	-	
3	Open Elective – II	2	-	3	Studied in III-Year II Semester
4	Professional Elective – II	3	-	3	Studied in III-Year II Semester
5	Professional Elective – III (Digital Design Verification with System Verilog)	3	PE-III (UNIX & Scripting Language)	3	
6	Computer Networks Lab	1	UNIX & Scripting Language LAB	2	
7	Antenna Simulation Lab	1	V & SV Lab/PD LAB(PC)	2	
8	Micro Wave and Optical Communications Lab	1			
9	Project – I	2	Project stage– I along with seminar presentation	3	
10	Summer Industry Internship - II	1			
IV/IV-B.Tech. ECE-Semester-II					
1	Professional Elective – IV (Digital Design and Verification with Universal Verification Methodology)	3	PE-IV (VLSID with Verilog/ X86 Architecture)	3	
2	Professional Elective – V (Mixed Signal Design)	3	PE-V (VLSI Synthesis/ RTOS)	3	
3	Open Elective – III	3	Open Elective – III (DFT/ARM Architecture & Interface Protocols/ Core JAVA/ Full Stack)	3	
4	Project – II	5	Project Stage– II	5	Every 2 credits of Project of Veda IIT will be

Suman M. A. L.
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					considered as 1 credit by SNIST.
			PE-VI		Shall be omitted for equivalency as PE-VI is not there in SNIST regulation
	Total Credits	34	Total Credits	34	

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 (Signature)

M. A. L. (Signature)
 M. K. R. (Signature)

(Signature)

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Minutes of BOS meeting held on 08.09.2022 in virtual mode:

I. The curriculum of SNIST (A18) has 8 credits for the following course under Program Core in IV Year I Semester:

- 1. Computer Networks - 3 credits
- 2. Microwave and Optical Communications - 2 credits
- 3. Computer Networks Lab - 1 credit
- 4. Antenna Simulation Lab - 1 credit
- 5. Micro Wave and Optical Communications Lab - 1 credit

All these courses under PC can be mapped with the following courses (under Program core) of Veda IIT which also amounts to 8 credits in IV Year I Semester:

- 1. Advanced Logic Design with functional blocks and state machine - 4 credits
- 2. UNIX & Scripting Language Lab - 2 credits
- 3. V & SV Lab/PD Lab - 2 credits

II. With these changes incorporated, the effective curriculum for IV Year I Semester for these 6 students under Veda IIT program shall be

- 1. Advanced Logic Design with functional blocks and state machine - 4 credits
- 2. UNIX & Scripting Language Lab - 2 credits
- 3. V & SV Lab/PD Lab - 2 credits
- 4. PE-III (Unix and Scripting language) - 3 credits
- 5. PE-IV - 3 credits
- 6. OE - II - 3 credits
- 7. Project stage- I with seminar - 3 credits

Total: 20 credits

III. By observing the IV Year II Semester course structure of SNIST (A18) and Veda IIT, the effective curriculum for IV Year II Semester for these 6 students under Veda IIT program shall be

- 1. Professional Elective-V - 3 credits
- 2. Professional Elective-VI - 3 credits
- 3. Open Elective-III - 3 credits
- 4. Project stage-II - 5 credits

(Every 2 credits of Project of Veda IIT will be considered as 1 credit under SNIST regulation and the students shall take the Technical Seminar as per the requirement of Veda HT)

Total: 14 credits

IV. Total number of credits in IV year under Veda IIT Program during IV Year : 40
Total number of credits in IV year under SNIST (A18) regulation during IV Year : 34
which is distributed as follows

IV Year I Semester - 20 credits
IV Year II Semester - 14 credits
Total - 34 credits

V. The PE-II and OE-II of IV Year I Semester under A18 (SNIST) regulation which have been studied in advance by the students in III Year under CBCS regulation shall not be considered for the award of the credits in the final provisional certificate.

VI. The professional electives shall be renamed as follows:

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- Blue signature "Srin" with a checkmark.
- Blue signature "M. A. ...".
- Black signature "P. ...".
- Green signature "M. ...".
- Blue signature "G. ...".
- Blue signature "M. ...".
- Red handwritten note: "which shall be considered as 1 credit under SNIST (A18) regulation".
- Blue handwritten note: "Srin".
- Green handwritten note: "12".

Veda IIT curriculum

PE-III
PE-IV
PE-V
PE-VI

SNIST (A18) curriculum

PE-II
PE-III
PE-IV
PE-V

VII. The elective subjects studied by these students at Veda IIT shall be specified in their provisional certificates.

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SREENIDHI INSTITUTE OF SCIENCE AND TECHNOLOGY
(Autonomous College)
Yamnampet, Ghatkesar, Hyderabad - 501 301
(Affiliated to JNTUH, Hyderabad)

Date: 08-09-2022

BOARD OF STUDIES MEETING

The BOS members of the B.Tech-ECE, SNIST have meet on **08-09-2022**

1. AGENDA

To finalise Curriculum mapping of SNIST (A18 regulation) with VEDA IIT - Industry-Ready Professional Training in VLSI Design/Embedded System Design as approved by JNTUH for credit transfer

2. Presentation of NBA evaluation report of NBA visit during April 2022

2. MEMBERS PRESENT






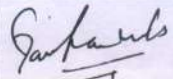
BOS External Members	
S. No.	Name of the Member
1	Dr. M. Asha Rani, Professor, JNTUH
2	Dr. P. Srihari, Associate Professor, NIT Warangal
3	Dr. P. Chandra Sekhar, Professor, Osmania University
4	Dr. G. Mallikarjun Rao, Scientist F, RCI
5	Mr. M. Pavan Kumar, Project Manager, Infosys
6	Mr Gaurav Kumar Wankar, Vice President of Product Management, SarvM.AI



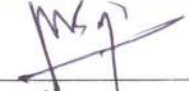

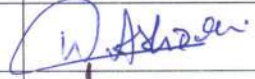




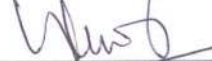
BOS Internal Members	
S. No.	Name of the Members
1	Dr. SPV. Subba Rao, Professor & Head ,BOS Chairman
2	Dr. CN. Sujatha, Professor , BOS Coordinator
3	Dr. G. Prasad Acharya, Associate Professor ,BOS Coordinator
4	Dr. NS. Murti Sarma, Professor
5	Dr. D. Asha Devi, Professor
6	Prof K Venkat Reddy , Professor
7	Dr. S. Krishna, Professor
8	Dr. J.Chatopadyaya, Professor.
9	Dr V Jaya Prakasan , Professor
10	Dr. T. Venkata Rao, Associate Professor
11	Dr V.Kumara Swamy, Associate Head.
12	.Dr V Rama Krishna Sharma , Associate Professor
13	Dr. T. Ramaswamy, Associate Professor
14	Mr N. Srinivas Reddy, Assistant Professor
15	Mr.Ch.Pamulati, Assistant Professor
16	Mr. S. N. Chandrasekhar, Assistant Professor
17	Mrs. T. Swapna, Assistant Professor
18	Mr. LVR Chaitanya Prasad , Assistant Professor
19	Mr.P.Anvesh, Assistant Professor
20	Mr.VSGN Raju, Assistant Professor





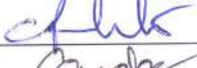
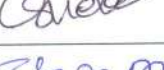
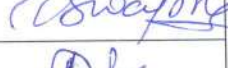


 Dr. S.P.V Subba Rao

Chairman BOS, SNIST

BOS External Members

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1	Dr. M. Asha Rani, Professor, JNTUH	
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3	Dr. P. Chandra Sekhar, Professor, Osmania University	
4	Dr. G. Mallikarjun Rao, Scientist F, RCI	
5	Mr. M. Pavan Kumar, Project Manager, Infosys	
6	Mr. Gaurav Kumar Wankar, (Vice President of Product - Server AIS)	

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6	Prof. K. Venkat Reddy, Professor	
7	Dr. S. Krishna, Professor	
8	Dr. Chatopadyaya, Professor.	
9	Dr. V. Jayaprakasan, Professor	
10	Dr. T. Venkat Rao, Associate Professor	

11	Dr. V. Kumara Swamy, Associate Professor	
12	Dr. V. Ramakrishna Sarma, Associate Professor	
13	Dr. T. Rama Swamy, Associate Professor	
14	Mr. N. Srinivas Reddy, Associate Professor	
15	Mr. Ch. Pamuleti, Associate Professor	
16	Mr. SN. Chandrashekar, Assistant Professor	
17	Mrs. T. Swapna, Assistant Professor	
18	Mr. LVR. Chaitanya Prasad, Assistant Professor	
19	Mr. P. Anvesh, Assistant Professor	
20	Mr. VSGN Raju, Assistant Professor	