

SREENIDHI INSTITUTE OF SCIENCE & TECHNOLOGY

M.TECH - I - YEAR - II SEM - (A17 REGULATIONS) SUPPLEMENTARY EXAMINATIONS, JAN / FEB, 2020

TIME TABLE

TIME a 10:15 AM TO 1:15 PM

BRANCH	DATE AND DAY					
	28.01.2020 Tuesday	30.01.2020 Thursday	01.02.2020 Saturday	04.02.2020 Tuesday	06.02.2020 Thursday	08.02.2020 Saturday
Electrical Power Engineering (EEE)	Power System Stability	Restructuring in Electrical Power Systems	Power Quality	Flexible AC Transmission Systems	Advanced Power System Protection	Banking, Operations, Insurance and Risk Management
CAD/CAM (ME)	Automation and Robotics	Optimum Design of Mechanical Elements	Flexible Manufacturing Systems and CAPP	Design for Manufacturing and Assembly	Nano Fluids for Energy Systems	Banking, Operations, Insurance and Risk Management
Thermal Engineering (ME)	Combustion and Environment	Computational Fluid Dynamics	Advanced Heat and Mass Transfer	Jet and Rocket Propulsion	Alternate Fuels	Banking, Operations, Insurance and Risk Management
Digital Systems And Computer Electronics (ECE)	Advanced Computer Architecture	Digital Signal Processor and Architecture / SOCA	Embedded and Real Time Operating Systems	Design of Fault Tolerant Systems	Low Power VLSI Design	Network Security and Cryptography
Computer Science & Engineering (CSE)	Web Technologies and Services	Network Security	Advanced Operating Systems	Mobile Computing / Machine Learning	Data Warehousing and Data Mining	Banking, Operations, Insurance and Risk Management
Computer Science (CS)	Internet Technologies and Services	Network Programming	Information Retrieval System	Mobile Computing	Data Mining	Banking, Operations, Insurance and Risk Management
Computer Network & Information Security (IT)	Adhoc and Sensor Networks	Network Security	Web Security and Ethical Hacking / BDA	Wireless Networks and Mobile Computing	Information Security, Management and Standards	Banking, Operations, Insurance and Risk Management

DATE: 10-01-2020

NOTE : ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY

Copy to:

- 1 The Executive Director / Principal for kind information
- 2 Controller of Examinations, Website I/C (Dr.Anup Kumar).
- 3 HoDs of CIVIL/EEE/ME/ECE/CSE/IT/ECM/BT/S&H with request to circulate among the students and arrange to place a copy on the notice board.
- 4 The SAP Section with a request to send SMS to parents & Students and also place it in the website.


DEAN, EXAMINATIONS

SREENIDHI INSTITUTE OF SCIENCE & TECHNOLOGY

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Digital Systems And Computer Electronics (ECE)	Advanced Computer Architecture	Digital Signal Processor and Architecture / System on Chip Architecture	Embedded Real Time Operating Systems	Design of Fault Tolerant Systems	Low Power VLSI Design	Entrepreneurship and Innovation
VLSI & Embedded Systems (ECE)	<i>CMOS Analog & Mixed Signal Design</i>	<i>System on Chip Architecture</i>	<i>Embedded Real Time Operating Systems</i>	<i>Design of Fault Tolerant Systems</i>	<i>Low Power VLSI Design</i>	<i>Entrepreneurship and Innovation</i>
Computer Science & Engineering (CSE)	Web Technologies & Services	Data Warehousing and Data Mining	Images Processing and Pattern Recognition	Mobile Application Development	Network Security and Cryptography	Banking, Operations, Insurance & Risk Management
Software Engineering (CSE)	Web Technologies & Services	Data Warehousing and Data Mining	Software Process and Project Management	Mobile Application Development	Software Architecture and Design Patterns	Banking, Operations, Insurance & Risk Management
Computer Network & Information Security (IT)	Web Technologies	Applications and Network Security	Web Security and Ethical Hacking	Wireless Networks and Mobile Computing	Information Security, Management and Standards	Banking, Operations, Insurance & Risk Management
Bio - Technology (BT)	Bioreactor Engineering	Food Biotechnology	Recombinant DNA Technology	Bioseparation Technology	Genomics and Proteomics	Biosafety & IPR

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