



Dr. Y.Sreenivasulu, IEEE Member

Assoc.Professor (ECE) and NAAC co-ordinator.

B.E. (ECE), M.Tech.(EC), Ph. D. (VLSI)

Career Highlights

22+ years of academia teaching (U.G and P.G) experience

| S.No | Name of the Intuition | Position Held | Duration |
|------|--|----------------------------------|---|
| 1. | SNIST, Hyderabad | Assoc.Prof | 22 nd Feb 2018 to till date |
| 2. | GVR & S College of Engg and Tech., Guntur. | Professor and HOD ECE Dept. | 2 nd March 2015 to Feb 21 st 2018 |
| 3. | SITS, Khammam. | Professor, TPO and HOD ECE Dept. | 1 st Sep. 2012 to 28 th Feb 2015. |
| 4. | HIET, Keesara, R.R. (D.T). | Professor and HOD ECE Dept. | June 2008 to August 2012. |
| 5. | NISITHA COLLEGE OF ENGG. & TECH, Lemoor, R.R. (D.T). | Assistant Professor in ECE Dept. | Aug. 2006 to Dec 2007. |
| 6. | S.R.T.I.S.T., Nalgonda. | Assistant Professor in ECE Dept. | Dec. 2005 to Jul 2006. |
| 7. | P.R.R.Polytechnic, Giddalure, Prakasam (D.T). | Associate Lecturer in ECE Dept. | Feb. 1992 to May 2003. |
| 8 | KAKATIYA Engineers Circle, Vijayawada. | Lecturer in the Dept. of ECE. | June 1989 to January 1992. |

Research Interests

Information security algorithm design in VLSI Circuit Design, And VLSI verification.

Courses Taught

Microprocessor and Microcontroller, Network Analysis, VLSI Design, Embedded Systems Design, , Digital System Design, STLD, Digital Integrated Circuits, Computer Architecture and Organization, PTSP and EMTL

Books and Chapters Published

Microprocessor and Microcontroller for Anuradha publications (under printing).

Education

Ph.D. - ECE Dept., In VLSI, A.U, Chidhambaram, Tamilanadu.

Exploration of fault free error coding technique for dynamic and static memory applications.

M.Tech. - ECE Dept. – EC, JNTUH, Hyderabad. India.

Bachelor of Engineering in Electronics, SVHCE, Machilipatnam, A.N. University, India 1989

