

Dr. Syed Jahangir Badashah.,

Associate Professor

B.E., M.E., Ph.D.,

Email – sydjahangir@sreenidhi.edu.in,

syd_jahangir@yahoo.co.in,

jahangirsyd@gmail.com

Mobile +91- 9849280509



ACADEMIC PROFILE

Qualification	University	Specialization
Ph.D	Sathyabama University	Electronics (Image processing)
M.E	Sathyabama University	Applied Electronics
B.E	Gulbarga University	Electronics & Communication Engg

Awards

Received Distinguished Faculty award in Engineering from Venus International Foundation in Chennai on 6th July 2019.

PATENT

1 “A System and Method for AI Based GPS Accredit Moratorium System” Application No. 201941038416 A, Publication Date 18/10/2019. <http://ipindiaservices.gov.in/publicsearch/newpublicsearch/index.php>

2.“Autonomous Floor Cleaning Apparatus” . Application No. 201841033568A Publication Date 14/09/2018.<http://ipindiaservices.gov.in/publicsearch/newpublicsearch/index.php>.

3.“Smart Compact Structural Instrument”. Application No. 201741036984A Publication Date 27/Oct/2017. <http://ipindiaservices.gov.in/publicsearch/newpublicsearch/index.php>

4 “A SMART COLOR WRITING TOOL FOR INTERACTIVE BOARDS”. Application No. 201741013344A Publication Date 21/04/2017. <http://ipindiaservices.gov.in/publicsearch/newpublicsearch/index.php>

WORK EXPERIENCE

- 1.Working in Sreenidhi Institute Science and Technology, Hyderabad, as Associate Professor in the Dept of ECE from February 2018 to till date.
- 2.Worked in CMR Engineering College, Hyderabad, as Professor in the Dept of ECE from December 2016 to January 2018.
- 3.Worked in Madina Engineering College, Kadapa as Associate Prof in the Dept of ECE from June 2005 to December 2016, also worked as University Examination head (EDEP) for four years.

BOOK

- 1.Gigabit Ethernet the Book's ISBN (978-3-330-07846-8).

Courses Taught

M.Tech

- 1.Embedded System Concepts.
 - 2.FPGA Architectures & Applications.
 - 3.Digital System Design.
 - 4.Network Security Cryptography.
1. Mixed Signal Lab

B.Tech

- 1.Electronic Devices and Circuits.
 - 2.Environmental Studies.
 - 3.Microprocessors and Interfacing.
 - 4.Embedded Systems.
 - 5.Analog Communications.
 - 6.Linear IC Applications.
 - 7.Digital Image Processing.
 - 8.Television Engineering.
- 1.EDC Lab
 2. Electronic Circuits.Lab
 - 3.Analog Communications.
 - 4.Pulse and Digital Circuits.Lab
 - 5.IC Applications and ECAD.Lab
 6. ECAD.Lab
 7. Digital Communications.Lab
 8. MW and OC.Lab

9. Embedded and Real Time Systems. 9. Embedded Systems. Lab
10. Microprocessors and Microcontroller 10. ECA Lab
11. Radar Systems. 11. MP & MC. Lab
12. Digital Communication. 12. MP & DSP Lab

INTERNATIONAL JOURNALS 13.

INTERNATIONAL CONFERENCES 6

NATIONAL JOURNALS 2

NATIONAL CONFERENCES 4

WORKSHOPS ATTENDED 14

EXTRA CURRICULAR ACTIVITIES

International Society Membership

1. FELLOW in International Society for Research and Development (ISFRAD) F3140900628.
2. Member in International Association of Engineers (IAENG) 188272.

National Society Membership

1. Life Member in "INDIAN SOCIETY FOR TECHNICAL EDUCATION" (ISTE) LM 58428.
2. Life Member in "The Institution of Electronics and Telecommunication Engineers" (IETE) M203198.

Editorial Board Member of a Journal/ Reviewer of International Journal 12