

**Dr. Abhishek Choubey, IEEE member VLSI society, Indian science congress**

Associate Professor (ECE)

B.E. (Electronics & Communication), M.Tech, Ph. D.



**Career Highlights**

11+years of academia teaching (U.G and P.G) experience

- (i) Sreenidhi institute of science & Technology, Hyderabad
- (ii) Jaypee University Engineering and Technology Guna
- (iii) RKDF-IST Bhopal
- (iv) MISCOS Pvt. Ltd, Bhopal

**Patents Filed& Awarded**

Patent application # 201941020883 - PATIENT MONITORING SYSTEM  
BASED ON EEG SIGNAL ACQUISITION, *Published* June 2019.

**Research Interests**

VLSI Architecture, low power, high performance and reconfigurable systems

**Courses Taught**

VLSI Design,  
Digital System Design,  
Digital Integrated Circuits,  
SDSD,  
Data communication  
Basic ELECTRONICS

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**Books and Chapters Published**

**Books-**

1. Shruti Bhargava, Abhishek Choubey, Mini Gupta, Significance of Independent Component Analysis in Imaging, LAMBERT Publications,ISBN 978-613-8-32446-1 .
2. Shruti Bhargava Choubey ,Abhishek Choubey,rishabah singh kurmi Structural design simulation of Advanced High Performance Bus protocol,LAMBERT Publications,ISBN-978-613-9-99119-8
3. Abhishek Choubey, Shruti Bhargava Choubey, FPGA Implementation of RISC Controller, LAMBERT academic Publication 978-613-9-98319-3.

**Book Chapters-**

1. Abhishek Choubey, Shruti Bhargava Choubey Khushboo Pachori Object Detection Using Higher Quality Optimization Techniques In Video Encoders, , Lecture Notes In Networks And Systems Springer Book Series,Pp 297-304 (Scopus Indexed)
2. Csn Koushik , Shruti Bhargava Choubey, Abhishek Choubey, Khushboo Pachori. A Literature Review On Quantum Experiments At Space Scale –Quess Satellite, Lecture Notes In Networks And Systems Springer Book Series.(Accepted, Scopus Indexed)
3. Csn Koushik , Shruti Bhargava Choubey, Abhishek Choubey, Spv Subba Rao, A Literature Review On Solid State Drives, Lecture Notes In Networks And Systems Springer Book Series. . (Accepted, Scopus Indexed)
4. V Vishnu Abhishek Choubey, A comparative Analysis of image steganography based on Discrete wavelet transform(DWT) and Exploiting modification direction(EMD) , Lecture Notes In Networks And Systems Springer Book Series. .(Accepted, Scopus Indexed)

## Education

**Ph.D. - ECE Dept., Jaypee University of Engineering and Technology, Guna, India, 2017**

Doctoral Dissertation – Parallel VLSI architecture for lifting 2D DWT using radix 8 booth multiplier.

**M.Tech. - ECE Dept. –Microelectronics and VLSI design , Rajiv Gandhi Prodyogiki Vishwavidyalaya, Bhopal (M.P.), India, 2009**

Master Thesis – FPGA implementation of 16 bit RISC microcontroller

**Bachelor of Engineering- Electronics & Communication, SRIT, RGPV, India 2006**

## Research & Entrepreneurship Grants and Industry Consultancy

1. Abhishek Choubey” FPGA implementation of DWT architecture” 80000 Rs. Funded by Sreenidhi Institute of Science & Technology, Hyderabad.
2. Abhishek choubey Design of a Smart Plant Health Monitoring System with Intelligent Gripper for Flower and Fruit Plucking 1,10000 Rs Funded by Sreenidhi Institute of Science & Technology, Hyderabad.

## Consultancy Project-

1. Shruti Bhargava & Abhishek choubey, Development of Intelligent Image & Video Processing Algorithm for Intelligent Video Cameras,1.45 Lakhs, Smart Bridge IOT.

No. of Journal Publications		Conference papers	FDP & Workshop attended	Teaching awards received
SCI/Scopus	Others			
05	24	07	07	1