

Dr. C N Sujatha, IEEE Member
 Professor (ECE), B.Tech. (ECE),
 M.Tech, Ph. D. (Sri Venkateswara University)
 Email–cnsujatha@sreenidhi.edu.in, cnsujatha@gmail.com
 Mb. – +91- 9949587845



Career Highlights

16+ years of academia teaching (U.G and P.G) experience

- **ECE Department, Sreenidhi Institute of Science and Technology (SNIST)**, Hyderabad, India
- **ECE Department, Annamacharya Institute of Technology & Science (AITS)**, Rajampet, Kadapa, Andhra Pradesh, India

Patents Filed & Awarded

1. **Patent Application No. 201931023191 A** - A device based upon an application for determining the location of a transport Vehicle at any instantaneous moment – **Date of filing of Application 12-06-2019 – Publication Date 09-08-2019**

Paper Publications

International journals	International conferences	National conferences
18	06	04

Research Interests

Signal Processing, Image & Video Processing, Machine Learning, Deep Learning and Antenna Design

Courses Taught

Signals & Systems, Pulse & Digital Circuits, Electronic Circuit Analysis, Microprocessor and Microcontroller, Digital Signal Processing, Digital Image Processing, Electronic Devices & Circuits, Basic Electronics, Electronic Circuits, Cellular Mobile Communication and Communication Systems

Books and Chapters Published

1. Co-Authored Book: A Design for Aperture Coupled Planar Array using Microwave Technology, ISBN – 978-3-659-64778-9
2. Authored Book: Secure Transmission of Text through Images with Encryption Algorithms, ISBN –978-620-2-07198-7
3. Authored Book Chapter: Invisible Color Image Authentication in SVD domain, Springer, DOI: 10.1007/978-3-319-63673-3_31
4. Authored Book Chapter: DWT based Blind Video Watermarking using Image Scrambling Technique, Springer Nature, DOI: 10.1007/978-981-13-1742-2_62

Ph.D. - ECE Dept. – Digital Image Processing, Sri Venkateswara University, India 2018

Doctoral Dissertation – Robust Hybrid Watermarking Schemes for Color Image & Video Authentication and Copyright Protection Using DWT-DCT-SVD

M.Tech. - ECE Dept. – LICS, Sri Venkateswara University, Tirupati, India 2005

Master Thesis - Design of Aperture Coupled Microstrip Planar Array, DLRL, Hyderabad

B.Tech. - ECE Dept. – VRSidhartha Engineering College, Nagarjuna University, Vijayawada, India 2001

Final Year Project– Secured Transmission using Cryptography Algorithm

Research & Development

S. No.	Title of R & D Project	Amount Approved by SNIST (Rs)	Duration	Status
1	Secure Transmission of image and videos for Copyright Protection	155000/-	Sept, 2015 to Jun, 2018	Completed
2	Implementation of Non Blind Video Authentication using LSB Scheme	110000/-	Feb, 2019	Going on

Academic and Institutional Responsibilities:

- BOS Coordinator
- In-charge for ECE Department Library
- In-charge for Digital Signal Processing Lab in ECE department
- Industry Oriented Mini Project Coordinator
- NBA Co-Coordinator
- Project Coordinator for B. Tech Final Year Project work
- Group-Project Coordinator for B.Tech
- Comprehensive Viva-voce Coordinator for B.Tech
- Active member in Organizing Short Term Courses, Seminars, Invited Lectures etc. in Department.
- In-charge of Departmental student disciplinary & anti-raging committee and Student Counseling & Mentoring Committee.
- Active member of Departmental Course File and Laboratory Manuals Reviewing Committee.